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1. PLAPACK Parallel Linear Algebra Package Design Overview

 Alpatov, P.; Baker, G.; Edwards, H.C.; Gunnels, J.; Morrow, G.; Overfelt, J.; van de Geijn, R.; [Supercomputing, ACM/IEEE 1997 Conference](#)

15-21 Nov. 1997 Page(s):29 - 29

Digital Object Identifier 10.1109/SC.1997.10004

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2. An Overview of the BlueGene/L Supercomputer

 Adiga, N.R.; Almasi, G.; Almasi, G.S.; Aridor, Y.; Barik, R.; Beece, D.; Bellofatto, R.; Bhanot, G.; Bickford, R.; Blumrich, M.; Bright, A.A.; Brunhereto, J.; Cascaval, C.; Castanos, J.; Chan, W.; Ceze, L.; Coteus, P.; Chatterjee, S.; Chen, D.; Chiu, G.; Cipolla, T.M.; Crumley, P.; Desai, K.M.; Deutsch, A.; Domany, T.; Dombrowski, M.B.; Donath, W.; Eleftheriou, M.; Erway, C.; Esch, J.; Fitch, B.; Gagliano, J.; Gara, A.; Garg, R.; Germain, R.; Giampaola, M.E.; Gopalsamy, B.; Gunnels, J.; Gupta, M.; Gustavson, F.; Hall, S.; Haring, R.A.; Heidel, D.; Heidelberger, P.; Herger, L.M.; Hoenicke, D.; Jackson, R.D.; Jamal-Eddine, T.; Kopcsay, G.V.; Krevat, E.; Kurhekar, M.P.; Lanzetta, A.P.; Lieber, D.; Liu, L.K.; Lu, M.; Mendell, M.; Misra, A.; Moatti, Y.; Mok, L.; Moreira, J.E.; Nathanson, B.J.; Newton, M.; Ohmacht, M.; Oliner, A.; Pandit, V.; Pudota, R.B.; Rand, R.; Regan, R.; Rubin, B.; Ruehl, A.; Rus, S.; Sahoo, R.K.; Sanomiya, A.; Schenfeld, E.; Sharma, M.; Shmueli, E.; Singh, S.; Song, P.; Srinivasan, V.; Steinmacher-Burow, B.D.; Strauss, K.; Surovic, C.; Swetz, R.; Takken, T.; Tremaine, R.B.; Tsao, M.; Umamaheshwaran, A.R.; Verma, P.; Vranas, P.; Ward, T.J.C.; Wazlowski, M.; Barrett, W.; Engel, C.; Drehmel, B.; Hilgart, B.; Hill, D.; Kasemkhan, F.; Krolak, D.; Li, C.T.; Liebsch, T.; Marcella, J.; Muff, A.; Okomo, A.; Rouse, M.; Schram, A.; Tubbs, M.; Ulsh, G.; Wait, C.; Wittrup, J.; Bae, M.; Dockser, K.; Kissel, L.; Seager, M.K.; Vetter, J.S.; Yates, K.; [Supercomputing, ACM/IEEE 2002 Conference](#)

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3. Large-Scale First-Principles Molecular Dynamics simulations on the BlueGene/L Platform using the Qbox code

 Gygi, F.; Yates, R.K.; Lorenz, J.; Draeger, E.W.; Franchetti, F.; Ueberhuber, C.W.; de Supinski, B.R.; Kral, S.; Gunnels, J.A.; Sexton, J.C.; [Supercomputing, 2005. Proceedings of the ACM/IEEE SC 2005 Conference](#)

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4. A high-performance SIMD floating point unit for BlueGene/L: architecture, compilation, and algorithm design

Bachega, L.; Siddhartha Chatterjee; Dockser, K.A.; Gunnels, J.A.; Manish Gupta; Gustavson,

F.G.; Lapkowski, C.A.; Liu, G.K.; Mendell, M.P.; Wait, C.D.; Ward, T.J.C.;
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5. **Unlocking the Performance of the BlueGene/L Supercomputer**
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6. **Fault-tolerant high-performance matrix multiplication: theory and practice**
Gunnels, J.A.; Katz, D.S.; Quintana-Orti, E.S.; Van de Geijn, R.A.;
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7. **PLAPACK: high performance through high-level abstraction**
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8. **A flexible class of parallel matrix multiplication algorithms**
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9. **Genetic algorithms and simulated annealing for gene mapping**
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1. A new efficient algorithm for solving differential-algebraic systems using implicit backward differentiation formulas

Brayton, R.K.; Gustavson, F.G.; Hachtel, G.D.;
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Volume 60, Issue 1, Jan. 1972 Page(s):98 - 108

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4. An Overview of the BlueGene/L Supercomputer

Adiga, N.R.; Almasi, G.; Almasi, G.S.; Aridor, Y.; Barik, R.; Beece, D.; Bellofatto, R.; Bhanot, G.; Bickford, R.; Blumrich, M.; Bright, A.A.; Brunheroto, J.; Cascaval, C.; Castanos, J.; Chan, W.; Ceze, L.; Coteus, P.; Chatterjee, S.; Chen, D.; Chiu, G.; Cipolla, T.M.; Crumley, P.; Desai, K.M.; Deutsch, A.; Domany, T.; Dombrowska, M.B.; Donath, W.; Eleftheriou, M.; Erway, C.; Esch, J.; Fitch, B.; Gagliano, J.; Gara, A.; Garg, R.; Germain, R.; Giampaapa, M.E.; Gopalsamy, B.; Gunnels, J.; Gupta, M.; Gustavson, F.; Hall, S.; Haring, R.A.; Heidel, D.; Heidelberger, P.; Herger, L.M.; Hoenicke, D.; Jackson, R.D.; Jamal-Eddine, T.; Kopcsay, G.V.; Krevat, E.; Kurhekar, M.P.; Lanzetta, A.P.; Lieber, D.; Liu, L.K.; Lu, M.; Mendell, M.; Misra, A.; Moatti, Y.; Mok, L.; Moreira, J.E.; Nathanson, B.J.; Newton, M.; Ohmacht, M.; Oliner, A.; Pandit, V.; Pudota, R.B.; Rand, R.; Regan, R.; Rubin, B.; Ruehli, A.; Rus, S.; Sahoo, R.K.; Sanomiya, A.; Schenfeld, E.; Sharma, M.; Shmueli, E.; Singh, S.; Song, P.; Srinivasan, V.; Steinmacher-Burow, B.D.; Strauss, K.; Surovic, C.; Swetz, R.; Takken, T.; Tremaine, R.B.; Tsao, M.; Umamaheshwaran, A.R.; Verma, P.; Vranas, P.; Ward, T.J.C.; Wazlowski, M.; Barrett, W.; Engel, C.; Drehmel, B.; Hilgart, B.; Hill, D.; Kasemkhani, F.; Krolak, D.; Li, C.T.; Liebsch, T.; Marcella, J.; Muff, A.; Okomo, A.; Rouse, M.; Schram, A.; Tubbs, M.; Ulsh, G.; Wait, C.; Wittrup, J.; Bae, M.; Dockser, K.; Kissel, L.; Seager, M.K.; Vetter, J.S.; Yates, K.;
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5. A high-performance SIMD floating point unit for BlueGene/L: architecture, compilation, and algorithm design

Bacheaga, L.; Siddhartha Chatterjee; Dockser, K.A.; Gunnels, J.A.; Manish Gupta; Gustavson, F.G.; Lapkowski, C.A.; Liu, G.K.; Mendell, M.P.; Wait, C.D.; Ward, T.J.C.; Parallel Architecture and Compilation Techniques, 2004. PACT 2004. Proceedings. 13th International Conference on

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6. Blue Gene/L, a system-on-a-chip

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- 11. An efficient parallel algorithm for the 3-D FFT NAS parallel benchmark
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- 12. A high performance parallel algorithm for 1-D FFT
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- 13. An active DC link filter for AC-AC converters
Larsson, T.; Ostlund, S.; Gustavson, F.;
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L2	21	cache\$1 and L1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/10 16:56
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L10	4	(LAPACK or (linear adj algebra adj pack\$3)) and BLAS and @ad<"20030929"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/10 17:12
L11	4	("5175837" "5717893" "5754888" "5790828").PN. OR ("6357041").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/04/10 17:11
L12	0	(("6" or six) adj (matrix adj subroutine\$1)) and @ad<"20030929"	US-PGPUB; USPAT; USOCR	OR	ON	2007/04/10 17:12
L13	4141964	((("6" or six) adj\$3 (matrix adj subroutine\$1)) and @ad<"20030929"	US-PGPUB; USPAT; USOCR	OR	ON	2007/04/10 17:12
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L16	8	(LAPACK or (linear adj algebra adj pack\$3)) and (matrix or matrices) and cache\$1 and @ad<"20030929"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/10 17:19
L17	12	("4858114" "5051745" "5142469" "5193174" "5212794" "5237689" "5257379" "5299322").PN. OR ("5944819").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/04/10 17:24
S1	29	(fred.in. and gustavson.in.) or (john.in. and gunnels.in.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/10 09:59

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S2	3	S1 and (select\$3 and (matrix or matrices) and plurality).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/10 10:28
S3	2	"5099447".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/10 10:30
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S5	344	select\$3 and (matrix or matrices) and @ad<"20030929" and cache\$1 and multiplication and subroutine\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/10 10:41
S6	21	cache\$1 and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/10 10:41
S7	7	S6 and @ad<"20030929"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/10 16:56

Inventor Information for 10/671935

Inventor Name	City	State/Country
GUSTAVSON, FRED GEHRUNG	BRIARCLIFF MANOR	NEW YORK
GUNNELS, JOHN A.	MT. KISCO	NEW YORK

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
09918814	6842765	150	07/31/2001	PROCESSOR DESIGN FOR EXTENDED-PRECISION ARITHMETIC	GUSTAVSON, FRED G.
08217533	Not Issued	161	03/24/1994	NUMERICALLY INTENSIVE COMPUTER ACCELERATOR	GUSTAVSON, FRED G.
08313970	5758176	250	09/28/1994	METHOD AND SYSTEM FOR PROVIDING A SINGLE-INSTRUCTION, MULTIPLE-DATA EXECUTION UNIT FOR PERFORMING SINGLE-INSTRUCTION, MULTIPLE-DATA OPERATIONS WITHIN A SUPERSCALAR DATA PROCESSING SYSTEM	GUSTAVSON, FRED G.
08313971	5513366	150	09/28/1994	METHOD AND SYSTEM FOR DYNAMICALLY RECONFIGURING A REGISTER FILE IN A VECTOR PROCESSOR	GUSTAVSON, FRED G.
08354402	5563818	150	12/12/1994	A METHOD AND SYSTEM FOR PERFORMING FLOATING-POINT DIVISION USING SELECTED APPROXIMATION VALUES	GUSTAVSON, FRED G.
08368171	Not Issued	166	01/04/1995	METHOD AND SYSTEM FOR ADDRESSING REGISTERS IN A DATA PROCESSING UNIT IN AN INDEXED ADDRESSING MODE	GUSTAVSON, FRED G.
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08368176	5890222	150	01/04/1995	METHOD AND SYSTEM FOR ADDRESSING REGISTERS IN A DATA PROCESSING UNIT IN AN INDIRECT ADDRESSING MODE	GUSTAVSON, FRED G.
08589770	5751619	250	01/22/1996	RECURRENT ARITHMETICAL COMPUTATION USING CARRY-SAVE ARITHMETIC	GUSTAVSON, FRED G.

08619456	5825677	250	03/20/1996	NUMERICALLY INTENSIVE COMPUTER ACCELERATOR	GUSTAVSON, FRED G.
08687825	5832533	250	07/26/1996	METHOD AND SYSTEM FOR ADDRESSING REGISTERS IN A DATA PROCESSING UNIT IN AN INDEXED ADDRESSING MODE	GUSTAVSON, FRED G.
10671887	Not Issued	71	09/29/2003	Method and structure for producing high performance linear algebra routines using composite blocking based on L1 cache size	GUSTAVSON, FRED GEHRUNG
10671888	Not Issued	71	09/29/2003	Method and structure for producing high performance linear algebra routines using register block data format routines	GUSTAVSON, FRED GEHRUNG
10671889	Not Issued	41	09/29/2003	Method and structure for producing high performance linear algebra routines using level 3 prefetching for kernel routines	GUSTAVSON, FRED GEHRUNG
10671933	Not Issued	71	09/29/2003	Method and structure for producing high performance linear algebra routines using a hybrid full-packed storage format	GUSTAVSON, FRED GEHRUNG
10671934	Not Issued	71	09/29/2003	Method and structure for producing high performance linear algebra routines using streaming	GUSTAVSON, FRED GEHRUNG
10671935	Not Issued	30	09/29/2003	Method and structure for producing high performance linear algebra routines using a selectable one of six possible level 3 L1 kernel routines	GUSTAVSON, FRED GEHRUNG
10671937	Not Issued	41	09/29/2003	Method and structure for producing high performance linear algebra routines using preloading of floating point registers	GUSTAVSON, FRED GEHRUNG
11035902	Not Issued	30	01/14/2005	Method and structure for a generalized cache-register file interface with data restructuring methods for multiple cache levels and hardware pre-fetching	GUSTAVSON, FRED GEHRUNG
11035933	Not Issued	30	01/14/2005	Method and structure for cache aware transposition via rectangular subsections	GUSTAVSON, FRED GEHRUNG
11039907	Not Issued	30	01/24/2005	Method and structure for algorithmic overlap in parallel processing for exploitation when load imbalance is dynamic and predictable	GUSTAVSON, FRED GEHRUNG
11045354	Not Issued	25	01/31/2005	Method and structure for a hybrid full-packed storage format as a single rectangular format data structure	GUSTAVSON, FRED GEHRUNG
11050945	Not Issued	30	02/07/2005	System and method for detecting a faulty object in a system	GUSTAVSON, FRED GEHRUNG
11052877	Not Issued	71	02/09/2005	System and method for algorithmic cache-bypass	GUSTAVSON, FRED GEHRUNG
11133254	Not Issued	25	05/20/2005	Method and structure for improving processing efficiency in parallel processing machines for rectangular and triangular	GUSTAVSON, FRED GEHRUNG

				matrix routines	
11328344	Not Issued	30	01/09/2006	Method and structure for an improved data reformatting procedure	GUSTAVSON, FRED GEHRUNG

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Last Name = GUNNELS

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
10671887	Not Issued	71	09/29/2003	Method and structure for producing high performance linear algebra routines using composite blocking based on L1 cache size	GUNNELS, JOHN A.
10671888	Not Issued	71	09/29/2003	Method and structure for producing high performance linear algebra routines using register block data format routines	GUNNELS, JOHN A.
10671889	Not Issued	41	09/29/2003	Method and structure for producing high performance linear algebra routines using level 3 prefetching for kernel routines	GUNNELS, JOHN A.
10671933	Not Issued	71	09/29/2003	Method and structure for producing high performance linear algebra routines using a hybrid full-packed storage format	GUNNELS, JOHN A.
10671934	Not Issued	71	09/29/2003	Method and structure for producing high performance linear algebra routines using streaming	GUNNELS, JOHN A.
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10671937	Not Issued	41	09/29/2003	Method and structure for producing high performance linear algebra routines using preloading of floating point registers	GUNNELS, JOHN A.
11035902	Not Issued	30	01/14/2005	Method and structure for a generalized cache-register file interface with data restructuring methods for multiple cache levels and hardware pre-fetching	GUNNELS, JOHN A.
11035933	Not Issued	30	01/14/2005	Method and structure for cache aware transposition via rectangular subsections	GUNNELS, JOHN A.
11039907	Not Issued	30	01/24/2005	Method and structure for algorithmic overlap in parallel processing for exploitation when load imbalance is dynamic and predictable	GUNNELS, JOHN A.
11041935	Not Issued	41	01/26/2005	Method and structure for high-performance linear algebra in the presence of limited outstanding miss slots	GUNNELS, JOHN A.
11045354	Not Issued	25	01/31/2005	Method and structure for a hybrid full-packed storage format as a single	GUNNELS, JOHN A.

				rectangular format data structure	
<u>11050945</u>	Not Issued	30	02/07/2005	System and method for detecting a faulty object in a system	GUNNELS, JOHN A.
<u>11052216</u>	Not Issued	41	02/08/2005	Method and structure for skewed block-cyclic distribution of lower-dimensional data arrays in higher-dimensional processor grids	GUNNELS, JOHN A.
<u>11052877</u>	Not Issued	71	02/09/2005	System and method for algorithmic cache-bypass	GUNNELS, JOHN A.
<u>11133254</u>	Not Issued	25	05/20/2005	Method and structure for improving processing efficiency in parallel processing machines for rectangular and triangular matrix routines	GUNNELS, JOHN A.
<u>11328344</u>	Not Issued	30	01/09/2006	Method and structure for an improved data reformatting procedure	GUNNELS, JOHN A.
<u>60377710</u>	Not Issued	159	05/03/2002	System and method for performing linear algebra	GUNNELS, JOHN A.

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